TOSHIBA Photocoupler GaAłAs IRed & Photo-IC

TLP113

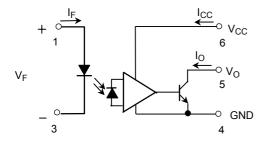
Isolated Line Receiver Simplex / Multiplex Data Transmission Computer-Peripheral Interface Microprocessor System Interface Digital Isolation For A / D, D / A Conversion

The TOSHIBA mini flat coupler TLP113 is a small outline coupler, suitable for surface mount assembly.

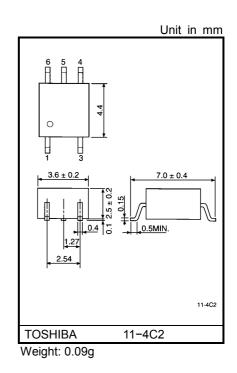
TLP113 consists of a GaAlAs light emitting diode, optically coupled to an integrated high gain, high speed photodetector whose output is an open collector, schottky clamped transistor.

- Input current thresholds: IF=10mA(max.)
- Switching speed: 10MBd(typ.)
- TTL / LSTTL compatible: VCC=5V
- Guaranteed performance over temp.: 0~70°C
- Isolation voltage: 2500Vrms(min.)
- UL recognized: UL1577 file no. E67349

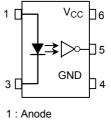
Schematic



(Note) A 0.1µF bypass capacitor must be connected between pins 4 and 6.



Pin Configuration(top view)



- 3 : Cathode
- 4 : GND
- 5 : Output
- (Open collector) 6 : V_{CC}

o:vCC

TRUTH TABLE (Positive Logic)

INPUT	OUTPUT
Н	L
L	Н

Maximum Ratings(Ta = 25°C)

	Characteristic	Symbol	Rating	Unit
	Forward current	١ _F	20	mA
	Pulse forward current (Note	e 1) I _{FP}	40	mA
LED	Peak transient forward current (Note	e 2)	1	А
	Reverse voltage		5	V
Output current Output voltage Supply voltage (1 minute maximum)	Output current	Ι _Ο	25	mA
	Output voltage	V _O	7	V
		V _{CC}	7	V
	Output power dissipation	PO	40	mW
Oper	perating temperature range		-40~85	°C
Stora	Storage temperature range		T _{stg} –55~125	
Lead	Lead solder temperature (10s)		260	°C
Isolation voltage (AC, 1 min., RH ≤ 60%, Note		e 4) BV _S	2500	Vrms

(Note 1) 50% duty cycle, 1ms pulse width.

(Note 2) Pulse width≤1µs, 300pps.

Recommende Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Input voltage, low level	V _{FL}	-3	0	1.0	V
Input current, high level	I _{FH}	13*	16	20	mA
Supply voltage	V _{CC}	4.5	5	5.5	V
Fan out (TTL load, each channel)	Ν	_	_	8	_
Operating temperature	T _{opr}	0	_	70	°C

* 13mA is a guard banded value which allows for at least 20% CTR degradation.

Initial input current threshold value is 10mA or less.

Electrical Characteristics(unless otherwise specified, Ta=0~70°C, V_{CC}=4.5~5.5V, V_{FL} \leq 1.0V)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit		
Forward voltage	V _F	I _F =10mA, Ta=25°C	_	1.65	1.80	V		
Forward voltage temperature coefficient	V _F / Ta	I _F =10mA	_	-2	_	mV / °C		
Reverse current	I _R	V _R =5V, Ta=25°C		—	10	μA		
Capacitance between terminals	CT	V _F =0, f=1MHz, Ta=25°C	_	45	_	pF		
High level output current	lou	V _F =1.0, V _O =5.5V		—	250	μA		
	Юн	V _F =1.0, V _O =5.5V, Ta=25°C		0.5	10	μΑ		
Low level output voltage	V _{OL}	I _F =10mA I _{OL} =13mA(sinking)	_	0.4	0.6	V		
"H level output→ L level output" input current	I _{FH}	I _{OL} =13mA(sinking) V _{OL} =0.6V	_	_	10	mA		
High level supply current	I _{ССН}	V _{CC} =5.5V, I _F =0		7	15	mA		
Low level supply current	I _{CCL}	V _{CC} =5.5V, I _F =16mA	_	12	18	mA		
Input-output insulation leakage current	IS	V _S =3540V, t=5s Ta=25°C (Note 4)	_	_	100	μΑ		
Isolation resistance	R _S	R.H. ≤ 60%, V _S =500V DC Ta=25°C (Note 4)	5×10 ¹⁰	10 ¹⁴	_	Ω		
Stray capacitance between input to output	C _S	V _S =0, f=1MHz Ta=25°C (Note 4)	_	0.8	_	pF		

* All typical values are V_{CC}=5V, Ta=25°C

Switching Characteristics (V_{CC}=5V, Ta=25°C)

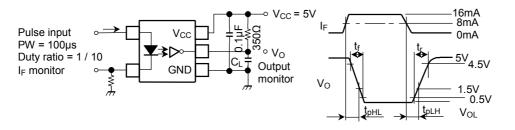
Characteristic	Symbol	Test Cir– cuit	Test Condition	Min.	Тур.	Max.	Unit
Propagation delay time (H→L)	t _p HL	1	I _F =0→16mA C _L =15pF, R _L =350Ω	_	60	120	ns
Propagation delay time (L→H)	t _p LH	1	I _F =16→0mA C _L =15pF, R _L =350Ω	_	60	120	ns
Output rise-fall time (10–90%)	t _r , t _f	2	R _L =350Ω, C _L =15pF I _F =0 祥 16mA		30		ns
Common mode transient imunity at high output level	CMH	2	I _F =0mA, V _{CM} =200V _{p-p} V _{O(min)} =2V, R _L =350Ω		200		V / μs
Common mode transient imunity at low output level	CML	2	I _F =16mA, V _{CM} =200V _{p-p} V _{O(max)} =0.8V, R _L =350Ω	_	-500	_	V / μs

(Note 4) Device considered a two-terminal device: Pins 1 and 3 shorted together, and pins 4, 5 and 6 shorted together.

(Note 5) The V_{CC} supply voltage to each TLP113 isolator must be bypassed by 0.1μ F capacitor, this can be either a ceramic or solid tantalum capacitor with good high frequency characteristic and should be connected as close as possible to package V_{CC} and GND pins of each device.

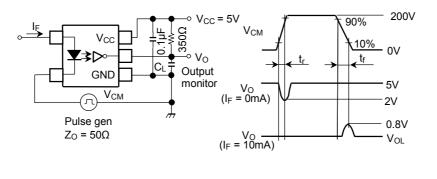
(Note 6) Maximum electrostatic discharge voltage for any pins: 180V(C=200pF, R=0)

Test Circuit 1: Switching Time Test Circuit



 C_{L} is approximately 15pF which includes probe and stray wiring capacitance.

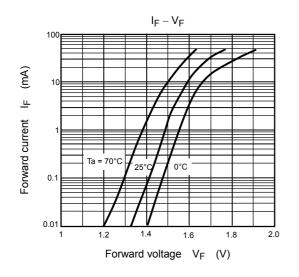
Test Circuit 2: Common Mode Transient Immunity Test Circuit

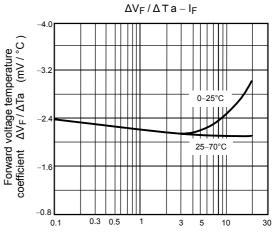


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CM_{H} = \frac{160(V)}{t_{f}(\mu s)}, CM_{L} = \frac{160(V)}{t_{f}(\mu s)}
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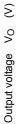
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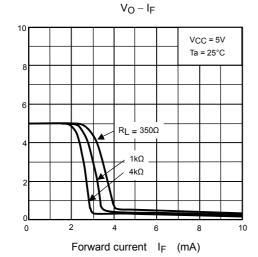
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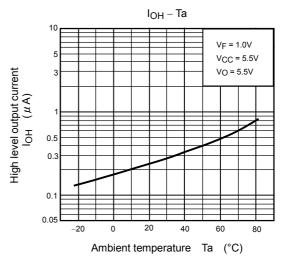




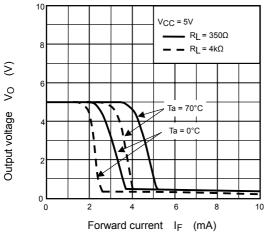
Forward current IF (mA)

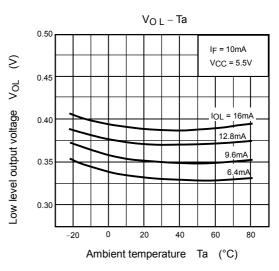




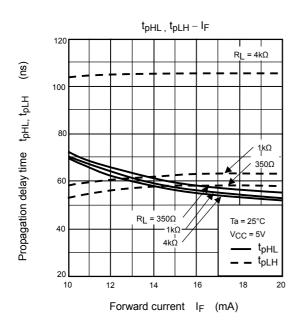


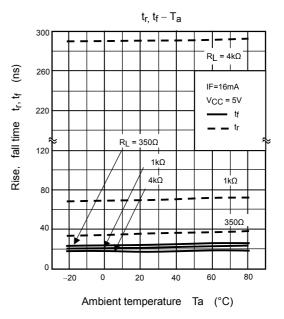




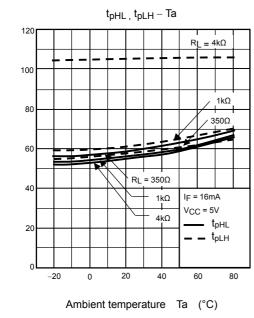


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Propagation delay time t_{pHL}, t_{pLH} (ns)



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